

for the embodiments of the present invention process regimes. The capping layer may be deposited at a temperature from 100-450 °C.--

IN THE CLAIMS:

Please amend the following claims.

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- 1 1. (Amended) A process for capping an extremely low dielectric constant
2 ("ELK") film comprising:
3 forming an ELK film on a substrate; and
4 depositing an amorphous silicon carbide capping layer on said ELK film.
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- 1 2. (Amended) The process of claim 1 further comprising forming a carbon-
2 doped silicon oxide capping layer on the amorphous silicon carbide capping layer, wherein the
3 carbon-doped oxide layer has a dielectric constant less than the dielectric constant of the
4 amorphous silicon carbide capping layer.
- 1 3. (Amended) The process of claim 1 wherein said amorphous silicon
2 carbide capping layer is hydrogenated, and has a dielectric constant less than approximately 4.5.
- A4 b8
- 1 8. (Amended) The process of claim 1 wherein a combined dielectric constant
2 for a stack comprising said ELK film and said silicon carbide capping layer is less than 3.0.
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- 1 10. (Amended) The process of claim 1 wherein said ELK film has a dielectric
2 constant of approximately 2.5 or less.
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- 1 13. (Amended) The process of claim 12 wherein said silicon containing
2 precursor comprises an organosilane compound.
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- 1 22. (Amended) A process for capping an extremely low dielectric constant
2 ("ELK") film using a silicon carbide material comprising:
3 forming an ELK film on a substrate; and

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would

4 depositing a silicon carbide capping layer having a dielectric constant of
5 approximately less than 5 on said ELK film, where said silicon-carbide layer is produced by a
6 process providing a silicon containing precursor, a carbon containing precursor and process
7 gases comprising oxygen, helium and nitrogen, and providing said silicon containing precursor
8 and said carbon containing precursor at a rate approximately six times that of the oxygen and
9 further comprising reacting said silicon and said carbon containing precursor in a chamber
10 having a pressure in the range of about 1 to 15 Torr with an RF power source supplying a power
11 at approximately 300-600 watts and a substrate surface temperature between approximately 100°
12 and approximately 450° C and having a shower head to substrate spacing of approximately 200
13 to approximately 600 mils, and wherein said capping layer has an adhesion strength of at least
14 about 35 MPa to said ELK film, and wherein the dielectric constant for a stack consisting of said
15 ELK film and said silicon carbide layer is at most approximately 3.0.

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2 25. (Amended) The stack of claim 24 wherein said amorphous silicon carbide
layer is a hydrogenated amorphous silicon carbide layer that has substantially no oxygen.

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2 34. (Amended) The stack of claim 23 wherein said amorphous silicon carbide
layer comprises an etch selectivity ratio of between about 40 to 1 and about 1 to 1, with respect
3 to the ELK layer.

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1 37. (Amended) The stack of claim 23 wherein said carbon-doped oxide layer
2 is produced by a process providing a silicon containing precursor, a carbon containing precursor
3 and process gases comprising oxygen, helium and nitrogen, and providing said silicon containing
4 precursor and said carbon containing precursor at rate approximately six times that of the oxygen
5 and further comprising reacting said silicon and said carbon in a chamber having pressure in the
6 range of about 1 to 15 Torr with an RF power source supplying a power at a rate of
7 approximately 300-600 watts and a substrate surface temperature between approximately 100°
8 and approximately 450° C and having a shower head to substrate spacing of approximately 200
9 to approximately 600 mils.

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1 39. (New) A process for capping a low dielectric constant film, the method
2 comprising:

3 forming a porous, low-dielectric constant film on a substrate; and
4 depositing a capping layer on the low-dielectric constant film, wherein the
5 capping layer comprises a carbon-doped oxide or an amorphous silicon carbide film, and
6 wherein the capping layer has a dielectric constant of about 5.0 or less.

1 40. (New) The process of claim 39 wherein the capping layer has a dielectric
2 constant of about 4.5 or less.

1 41. (New) The process of claim 39 wherein the capping layer is in direct
2 contact with the porous, low-dielectric constant film.

1 42. (New) The process of claim 39 wherein the porous, low-dielectric constant
2 film has a dielectric constant less than about 2.5.

1 43. (New) The process of claim 39 wherein the porous, low dielectric constant
2 film and the capping layer are in a stack of layers, and wherein the stack of layers has an
3 effective dielectric constant less than about 3.0.